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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Inventor: Kang-yoon Lee et al.

Group Art Unit: 2825

Serial No.: 10/057,745

Confirmation No.: 2804

Filed: October 26, 2001

Examiner: Chuong A. Luu

For: INTEGRATED CIRCUIT DEVICES HAVING ACTIVE REGIONS WITH
EXPANDED EFFECTIVE WIDTHS AND METHODS OF MANUFACTURING THE SAME

Date: February 24, 2003

BOX NON-FEE AMENDMENT
Commissioner for Patents
Washington, DC 20231

TECHNOLOGY CENTER 2800

MAR 17 2003

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AMENDMENT

Sir:

This is a full and timely response to the non-final Office Action mailed November 25, 2002 that contains amendments and remarks as set forth hereafter. Attached hereto is a marked up version of the changes made to the claims by the current amendments. The marked up version of the changes is captioned "**VERSION WITH MARKINGS TO SHOW CHANGES MADE.**"

In the Claims:

Please enter amended Claims 10, 15, and 31 as follows:

OFFICE OF CHIEF OF RECORDS
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FINANCE SECTION

10. (Amended) A method of manufacturing an integrated circuit device,
comprising:

forming a trench in a substrate;

forming an isolation layer comprising an insulating material in the trench so as to
cover a first sidewall portion of the trench and an entire bottom of the trench; and

forming a gate electrode on a second sidewall portion of the trench.

15. (Amended) A method of manufacturing an integrated circuit device,
comprising:

etching a substrate to form a trench and a mesa therein, the mesa comprising an upper
surface and a sidewall, which is adjacent the trench;

filling an entire bottom of the trench with an insulating material so as to cover a first